| Electronic Acknowledgement Receipt   |   |  |  |  |
|--------------------------------------|---|--|--|--|
| EFS ID:                              | 1275267   |  |  |  |
| Application Number:                  | 10719878  |  |  |  |
| International Application Number:    |   |  |  |  |
| Confirmation Number:                 | 3082  |  |  |  |
| Title of Invention:                  | Device and method for using a lessened load to measure signal skew at the output of an integrated circuit |  |  |  |
| First Named Inventor/Applicant Name: | Jeffrey S. Brown  |  |  |  |
| Correspondence Address:              | Leo Peters LSI Logic Corporation 1621 Barber Lane, MS D-106 - Milipitas CA 95035 US -                     |  |  |  |
| Filer:                               | Kevin L. Daffer/Pamela Gerik  |  |  |  |
| Filer Authorized By:                 | Kevin L. Daffer   |  |  |  |
| Attorney Docket Number:              | 5201-27900 03-0940  |  |  |  |
| Receipt Date:                        | 26-OCT-2006   |  |  |  |
| Filing Date:                         | 21-NOV-2003   |  |  |  |
| Time Stamp:                          | 12:25:21  |  |  |  |
| Application Type:                    | Utility   |  |  |  |
| Payment information:                 |   |  |  |  |
| Submitted with Payment               | no  |  |  |  |

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| Warnings:          |   |                 |                  |                     |                     |  |  |
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## New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

## National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.